Form PTO 1449 (Modified)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 247252US2S DIV		SERIAL NO. New Div. Application
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Keiji HOSOTANI		
		FILING DATE HEREWITH		GROUP

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
✓	AA	6,072,718	06/06/00	ABRAHAM ET AL			
	AB	2001/0035545	11/01/01	SCHUSTER-WOLDAN ET AL			
	AC	5,936,882	08/10/99	DUNN			
	AD	6,048,739	04/11/02	HURST ET AL			
	AE	6,278,631	08/21/01	NAJI			
	AF	5,883,828	03/16/99	CUCHIARO ET AL			
	AG	2002/0039308	04/04/2002	GOGL ET AL			
	AH	6,522,573	02-2003	SAITO ET AL			
	AI	6,421,271	07-2002	GOGI ET AL			
	AJ	6,211,559	04-2001	ZHU ET AL			
	AK	6,538,297	03-2003	ODAGAWA ET AL			
✓	AL	6,365,419	04/02/02	DURLAM ET AL			
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO
✓	AO	2001-357666	12/26/01	JAPAN		X
✓	AP	2001-217398	08/10/01	JAPAN (w/ENGLISH ABSTRACT)		X
✓	AQ	2002-157874	05/31/02	JAPAN (w/ENGLISH ABSTRACT)		x
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

✓	AW	Roy SCHEUERLEIN, et al., "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in each Cell", ISCC 2000/SESSION 7/TD: EMERGING MEMORY & DEVICE TECHNOLOGIES/PAPER TA 7.2, 2000 IEEE International Solid-State Circuits Conference, February 8, 2000, pgs. 126-133.
✓	AX	Peter K. NAJI, et al., "A 256kb 3.0V 1T1MTJ Nonvolatile Magnetoresistive RAM", ISSCC 2001/SESSION 7/TECHNOLOGY DIRECTIONS: ADVANCED TECHNOLOGIES/7.6, TECHNOLOGY DIGEST, 2001 IEEE International Solid-State Circuits Conference, 11 pgs.
	AY	
	AZ	☐ Additional References sheet(s) attached

Examiner		Date Considered 10/14/0

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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